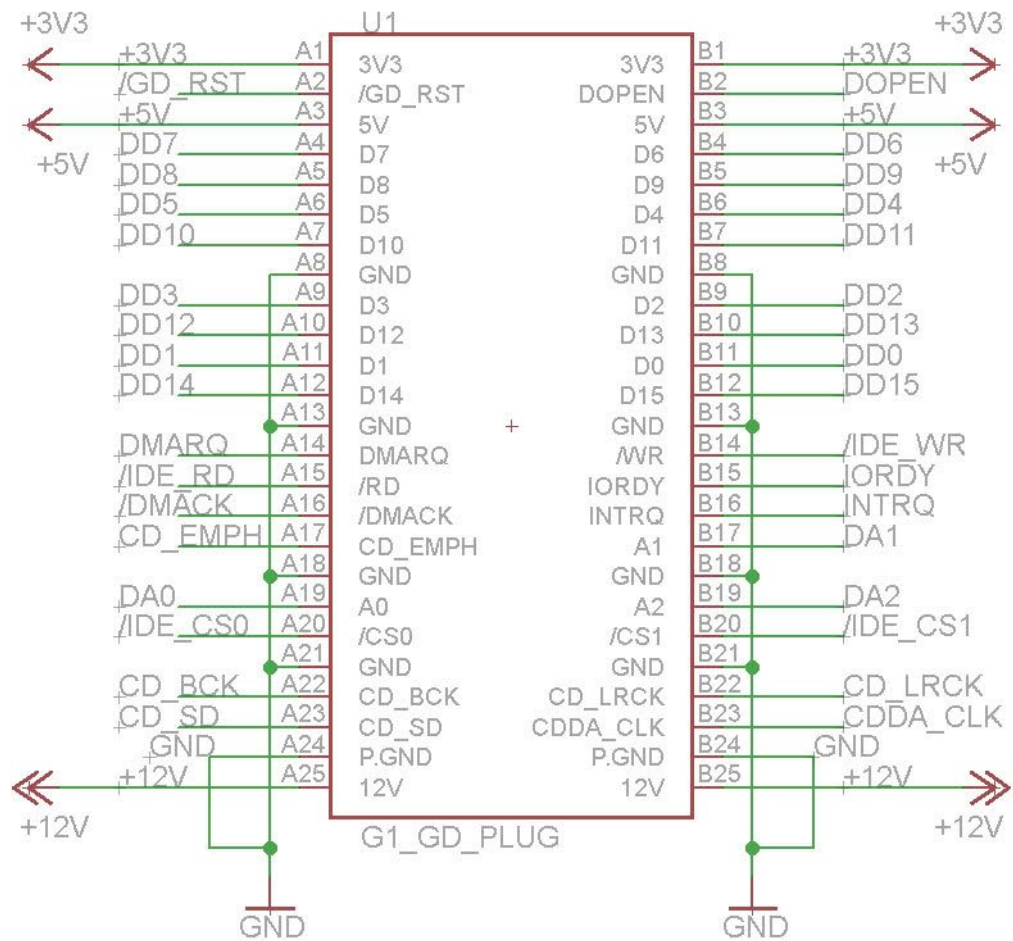


GD-ROM Drive Interface



Host Atapi DC GD-ROM connector pin out							GD-ROM memory map				
Level	type	name	pin	pin	name	type	Level	offset mem base 0xA05F7000	reg	chip select	data bus
3V3	PWR	VCC3V3	A1	B1	VCC3V3	PWR	5v: open 0v: close	0x18	IDE_ALTSTATUS	CS_GDROM_ALT#	8bits
	O	RESET#	A2	B2	DOOR_OPEN	I		0x1c	IDE_UNKNOWN	CS_GDROM_ALT#	8bits
	PWR	VCC5V	A3	B3	VCC5V	PWR		0x38	IDE_ALTSTATUS	CS_GDROM_ALT#	8bits
3V3	IO	D7	A4	B4	D6	IO	3V3	0x3c	IDE_UNKNOWN	CS_GDROM_ALT#	8bits
3V3	IO	D8	A5	B5	D9	IO	3V3	0x3e	IDE_UNKNOWN	CS_GDROM_ALT#	8bits
3V3	IO	D5	A6	B6	D4	IO	3V3	0x58	IDE_ALTSTATUS	CS_GDROM_ALT#	8bits
3V3	IO	D10	A7	B7	D11	IO	3V3	0x5c	IDE_UNKNOWN	CS_GDROM_ALT#	8bits
3V3	PWR	GND	A8	B8	GND	PWR	3V3	0x78	IDE_ALTSTATUS	CS_GDROM_ALT#	8bits
	IO	D3	A9	B9	D2	IO		0x7c	IDE_UNKNOWN	CS_GDROM_ALT#	8bits
	IO	D12	A10	B10	D13	IO		0x80	IDE_DATA	CS_GDROM#	16bits
3V3	IO	D1	A11	B11	D0	IO	3V3	0x84	IDE_FEATURE	CS_GDROM#	8bits
3V3	IO	D14	A12	B12	D15	IO	3V3	0x88	IDE_COUNT	CS_GDROM#	8bits
	PWR	GND	A13	B13	GND	PWR	3V3	0x8c	IDE_LBA0	CS_GDROM#	8bits
	I	DMARQ	A14	B14	AWR	O		0x90	IDE_LBA1	CS_GDROM#	8bits
	O	RD#	A15	B15	IORDY	I		0x94	IDE_LBA2	CS_GDROM#	8bits
3V3	O	DMACK#	A16	B16	INTRQ	I	3V3	0x98	IDE_DEVICE	CS_GDROM#	8bits
5V	I	CD_EMPH	A17	B17	A1	O	3V3	0x9c	IDE_COMMAND	CS_GDROM#	8bits
3V3	PWR	GND	A18	B18	GND	PWR	3V3	0xA0	IDE_DATA	CS_GDROM#	16bits
	O	A0	A19	B19	A2	O		0xA4	IDE_FEATURE	CS_GDROM#	8bits
	O	CS_CS0#	A20	B20	CS_CS1#	O		0xA8	IDE_COUNT	CS_GDROM#	8bits
5V	PWR	GND	A21	B21	GND	PWR	5V	0xAC	IDE_LBA0	CS_GDROM#	8bits
	I	CD_BCK (1.41Mhz)	A22	B22	CD_LRCK (44.1Khz)	I		0xB0	IDE_LBA1	CS_GDROM#	8bits
	I	CD_SD (audio data)	A23	B23	CDDA_CLK (33Mhz)	I		0xB4	IDE_LBA2	CS_GDROM#	8bits
5V	PWR	P.GND	A24	B24	GND	PWR	PWR	0xB8	IDE_DEVICE	CS_GDROM#	8bits
	PWR	VCC12V	A25	B25	VCC12V	PWR		0xBC	IDE_COMMAND	CS_GDROM#	8bits
	Note: the pinout is relative to the DreamCast motherboard.							CS_GDROM#	8bits
								0xFC	IDE_COMMAND	CS_GDROM#	8bits
								0x118	IDE_ALTSTATUS	CS_GDROM_ALT#	8bits
								0x11c	IDE_UNKNOWN	CS_GDROM_ALT#	8bits
								0x138	IDE_ALTSTATUS	CS_GDROM_ALT#	8bits
								0x13c	IDE_UNKNOWN	CS_GDROM_ALT#	8bits
								0x158	IDE_ALTSTATUS	CS_GDROM_ALT#	8bits
								0x15c	IDE_UNKNOWN	CS_GDROM_ALT#	8bits
								0x178	IDE_ALTSTATUS	CS_GDROM_ALT#	8bits
								0x17c	IDE_UNKNOWN	CS_GDROM_ALT#	8bits
								0x180	IDE_DATA	CS_GDROM#	16bits
								0x184	IDE_FEATURE	CS_GDROM#	8bits
								0x188	IDE_COUNT	CS_GDROM#	8bits
								0x18c	IDE_LBA0	CS_GDROM#	8bits
								0x190	IDE_LBA1	CS_GDROM#	8bits
								0x194	IDE_LBA2	CS_GDROM#	8bits
								0x198	IDE_DEVICE	CS_GDROM#	8bits
								0x19c	IDE_COMMAND	CS_GDROM#	8bits
								CS_GDROM#	8bits
								0x3FC	IDE_COMMAND	CS_GDROM#	8bits
								0xA05F7400			

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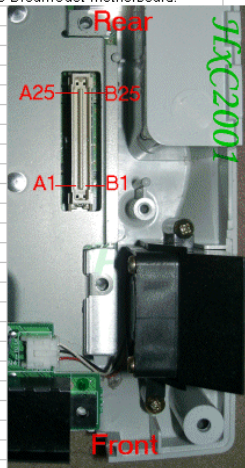
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A25-----B25

A1-----B1

Front

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Revision #2

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